



1/8

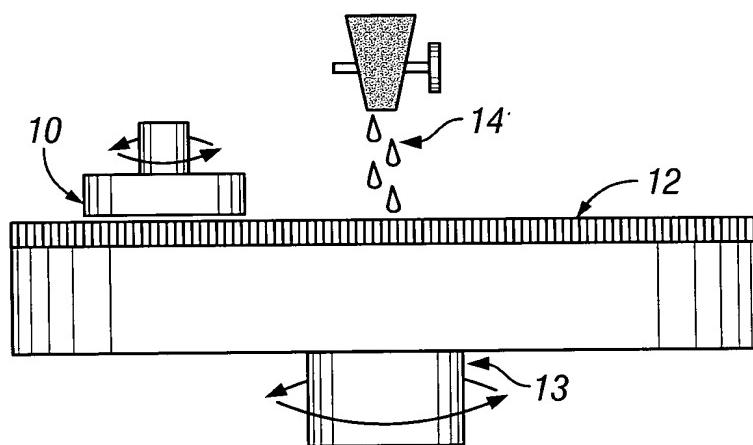


FIG. 1
(Prior Art)

FIG. 2

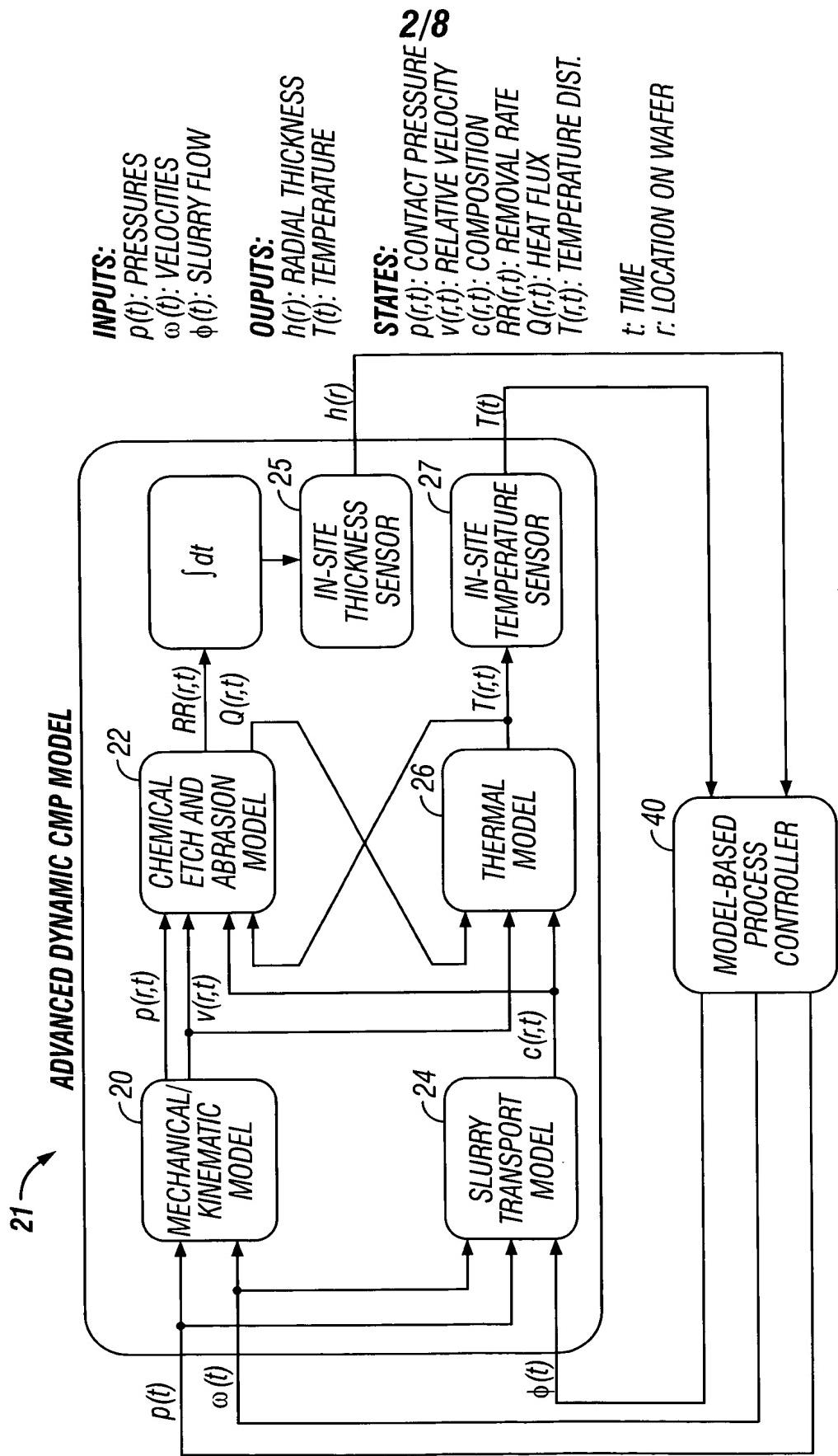
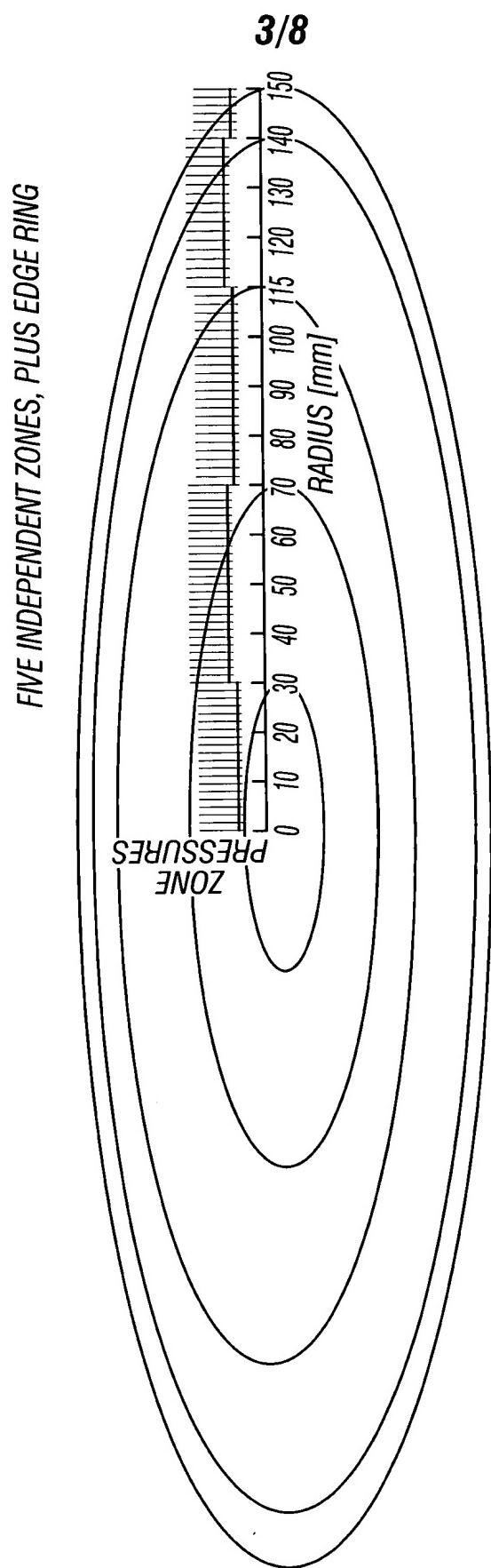


FIG. 3



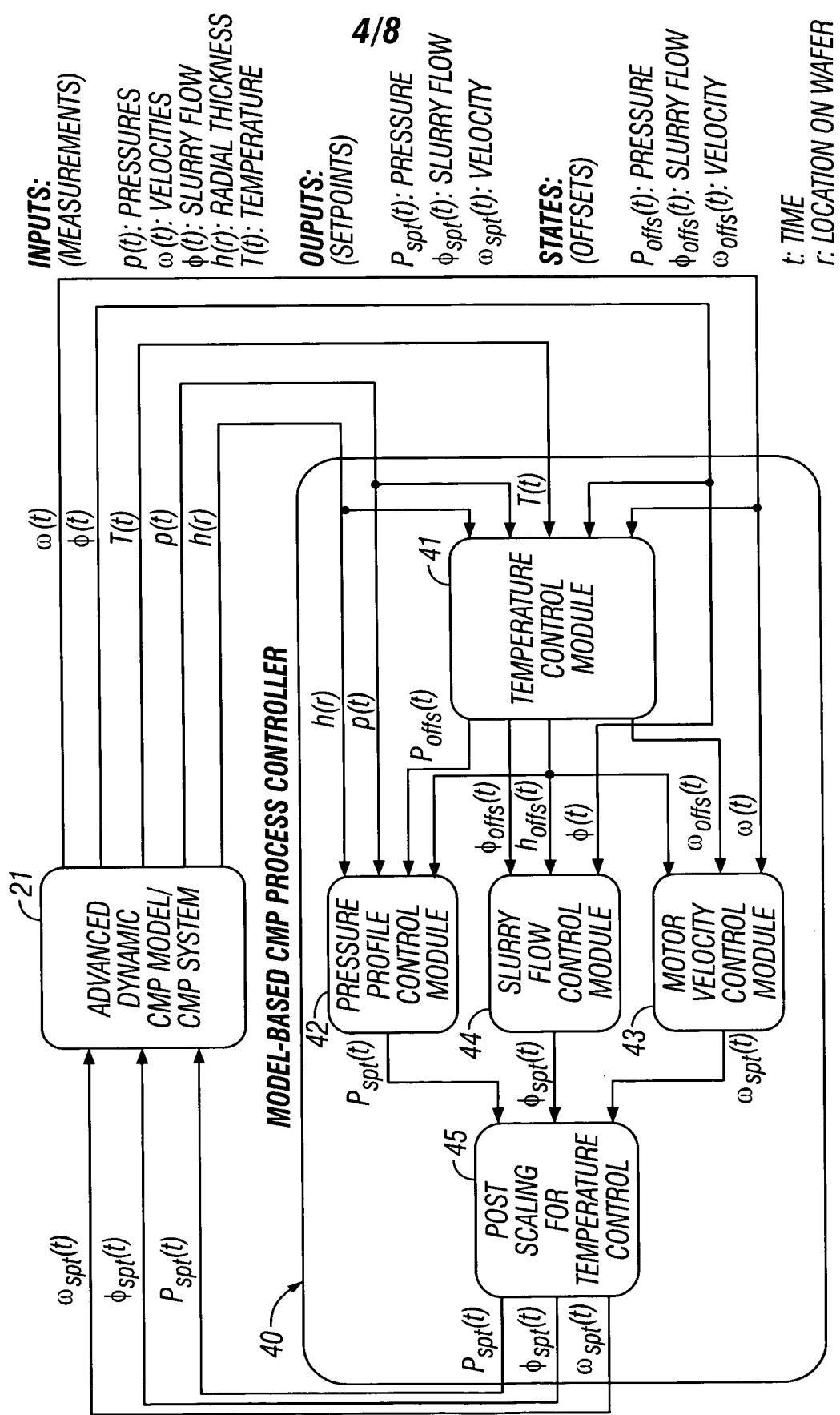


FIG. 4

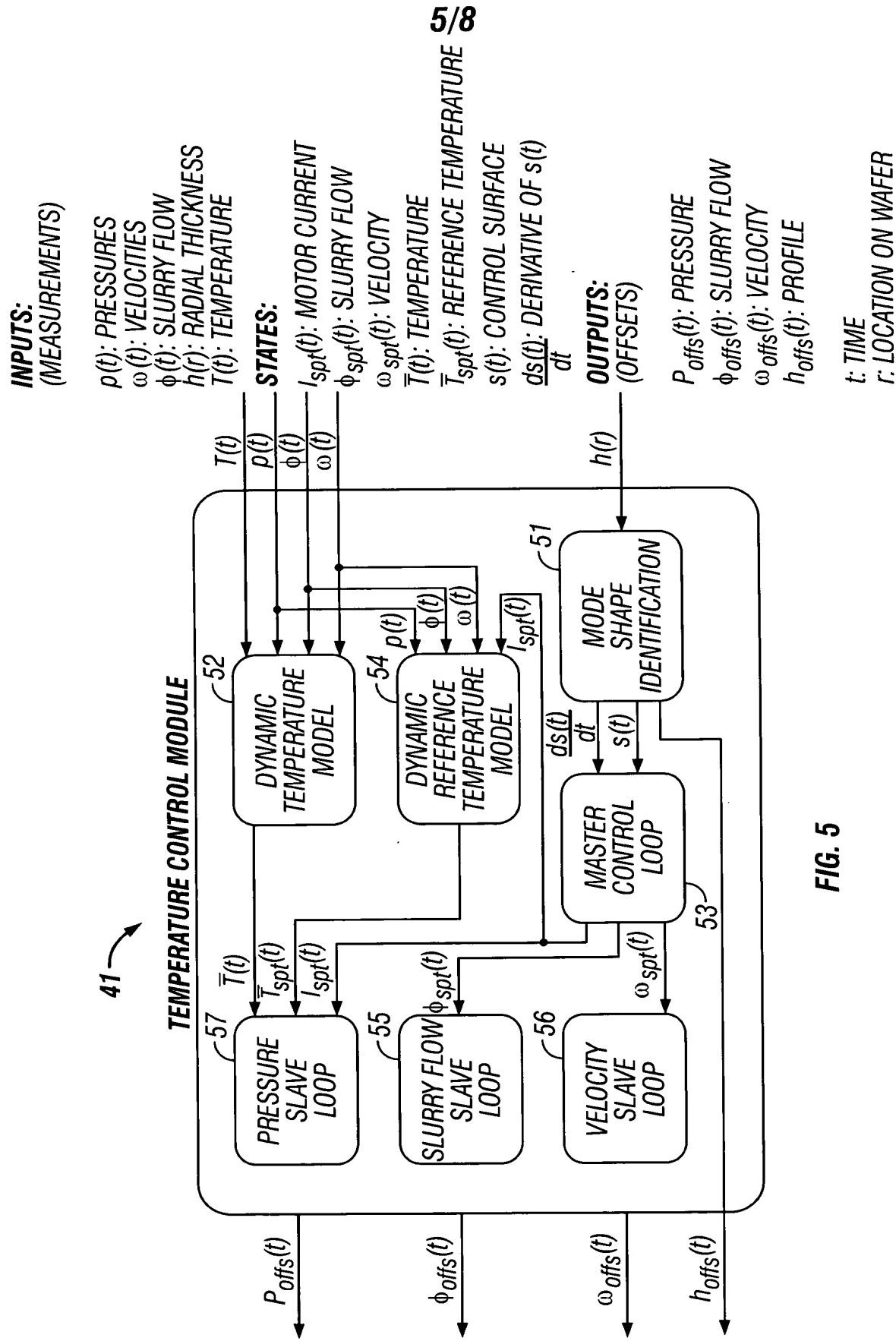


FIG. 5

t: TIME
r: LOCATION ON WAFER

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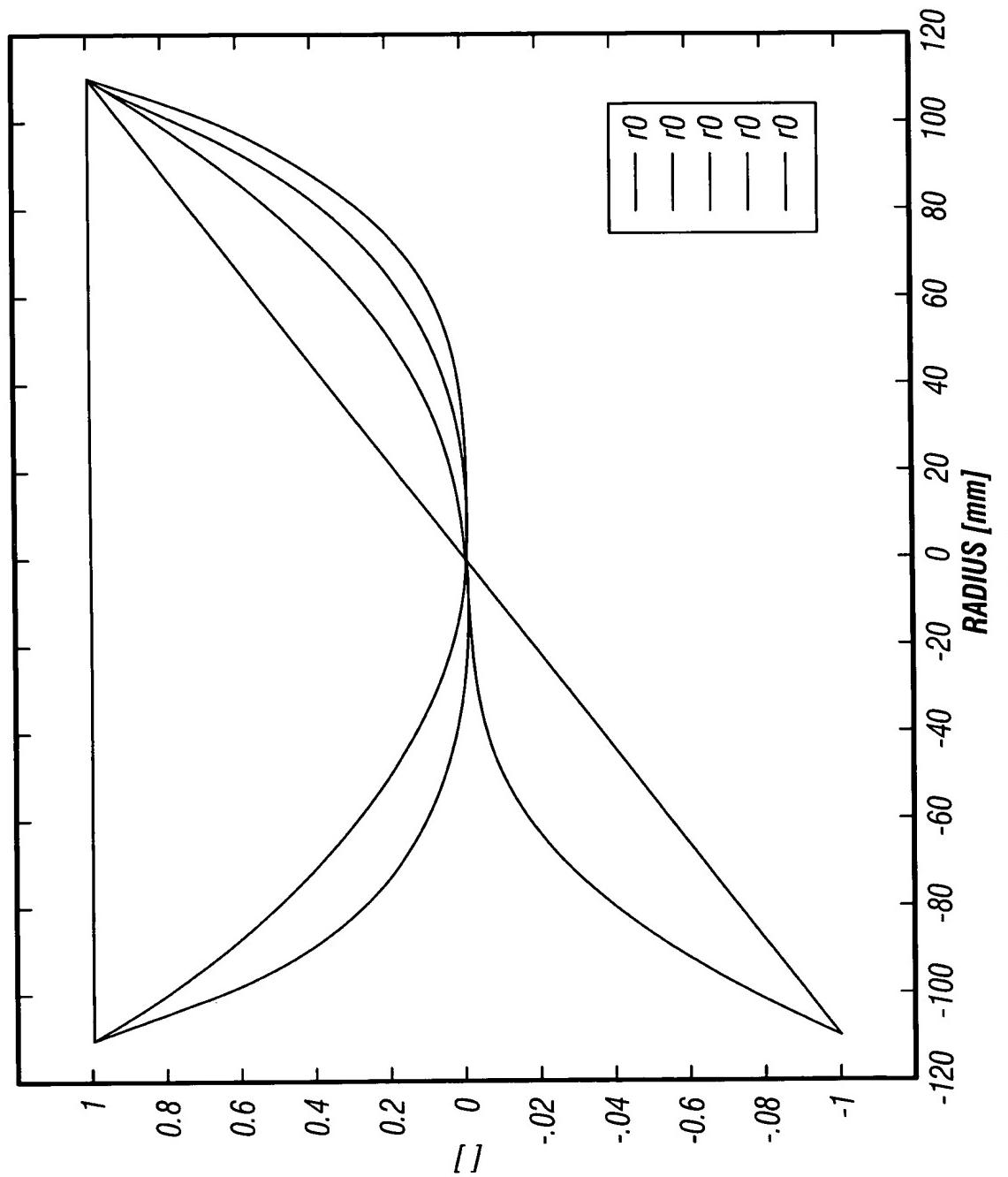
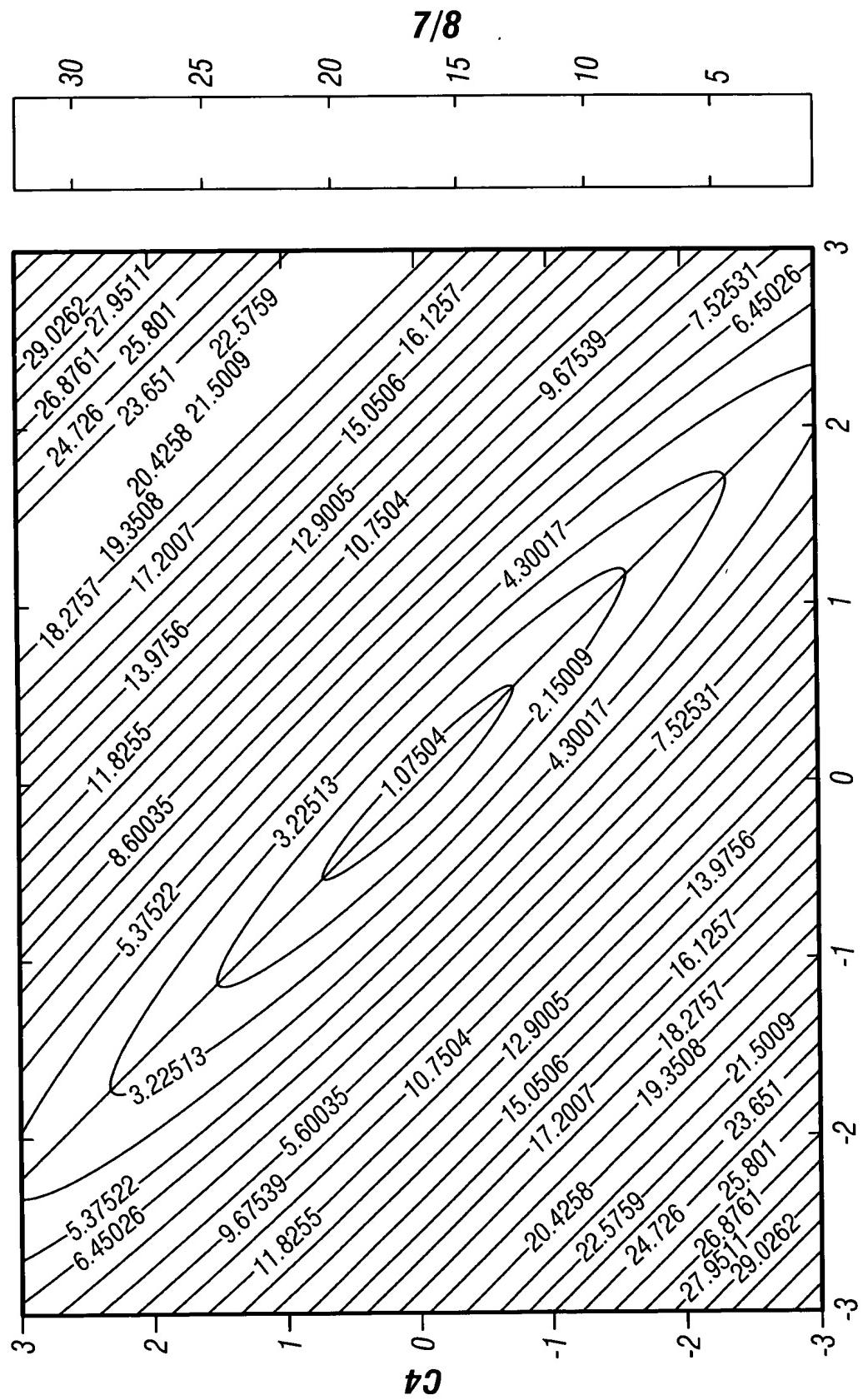


FIG. 6

FIG. 7



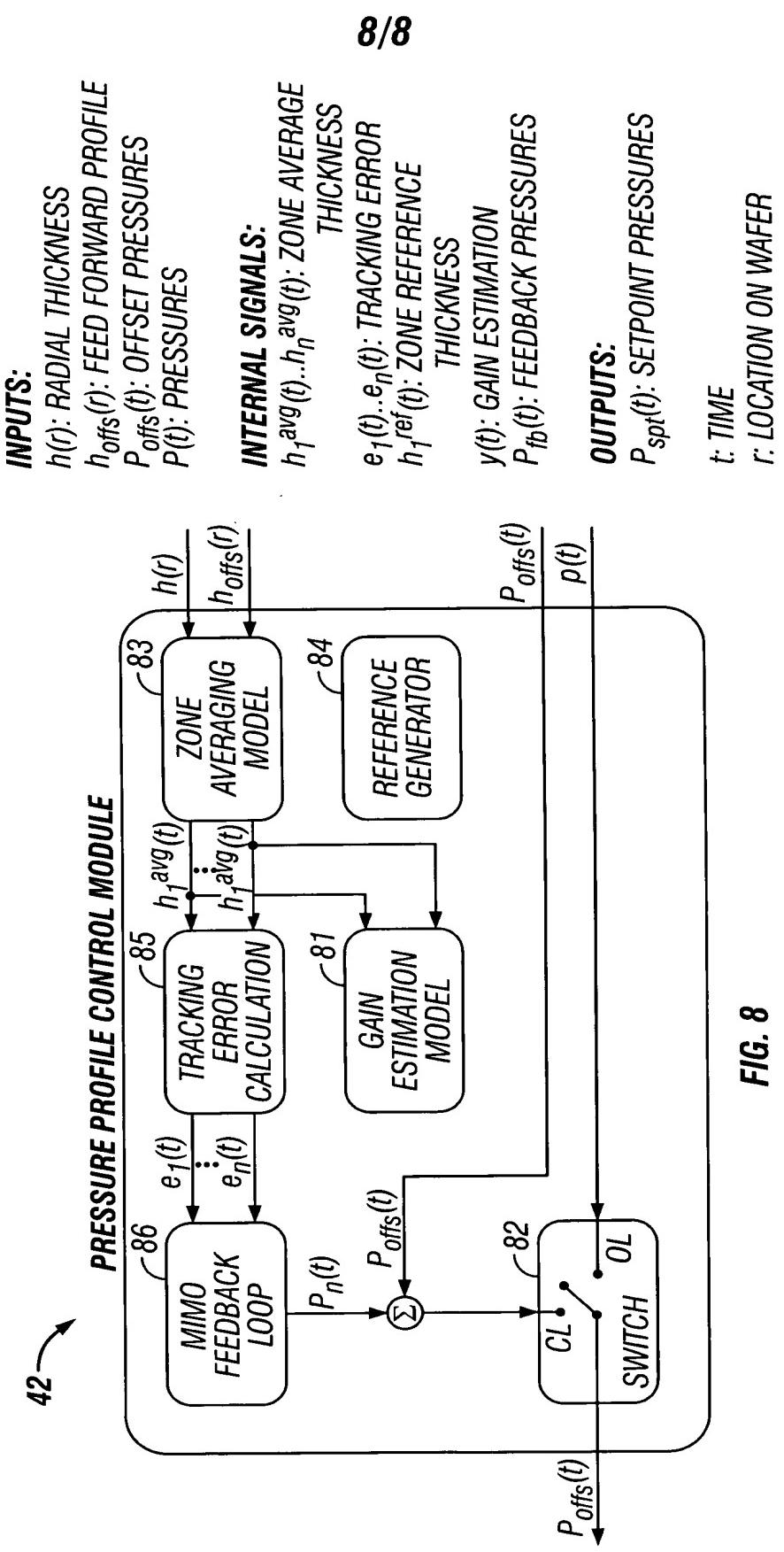


FIG. 8

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